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NOTICE OF ALLOWANCE AND FEE(S) DUE

30431

7590

04/25/2008

STMICROELECTRONICS, INC. MAIL STATION 2346 1310 ELECTRONICS DRIVE CARROLLTON, TX 75006 EXAMINER

NEGRON, DANIELL L

ART UNIT PAPER NUMBER

2627

DATE MAILED: 04/25/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/993.986	11/05/2001	Fereidoon Heydari	01-S-045 (1678-47)	7945

TITLE OF INVENTION: CIRCUIT AND METHOD FOR DEMODULATING A SERVO POSITION BURST

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$300	\$0	\$1740	07/25/2008

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

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A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

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B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

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III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

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Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

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maintenance fee notifications. Note: A certificate of mailing can only be used for domestic mailings of the CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address) Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission. 30431 7590 04/25/2008 Certificate of Mailing or Transmission STMICROELECTRONICS, INC. I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below. MAIL STATION 2346 1310 ELECTRONICS DRIVE CARROLLTON, TX 75006 (Depositor's name (Signature (Date APPLICATION NO. FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. FILING DATE 09/993,986 11/05/2001 Fereidoon Heydari 01-S-045 (1678-47) 7945 TITLE OF INVENTION: CIRCUIT AND METHOD FOR DEMODULATING A SERVO POSITION BURST APPLN. TYPE SMALL ENTITY ISSUE FEE DUE PUBLICATION FEE DUE PREV. PAID ISSUE FEE TOTAL FEE(S) DUE DATE DUE nonprovisional NO \$1440 \$300 \$0 \$1740 07/25/2008 **EXAMINER** ART UNIT CLASS-SUBCLASS NEGRON, DANIELL L 2627 360-029000 1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). 2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. or agents OR, alternatively, (2) the name of a single firm (having as a member a ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required. registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type) PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment. (A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY) 4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above) 4a. The following fee(s) are submitted: lssue Fee A check is enclosed. Publication Fee (No small entity discount permitted) Payment by credit card. Form PTO-2038 is attached. The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number ______ (enclose an extra copy of this fo Advance Order - # of Copies _ (enclose an extra copy of this form). 5. Change in Entity Status (from status indicated above) a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ■ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2). NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office. Authorized Signature Date Typed or printed name Registration No. This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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STMICROELECTRONICS, INC.			NEGRON, DANIELL L	
MAIL STATION 2346			ART UNIT	PAPER NUMBER
1310 ELECTRON CARROLLTON, 7	=		2627 DATE MAILED: 04/25/200	8

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 322 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 322 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

	Application No.	Applicant(s)
	09/993,986	HEYDARI ET AL.
Notice of Allowability	Examiner	Art Unit
	Daniell L. Negrón	2627
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communicatio IGHTS. This application is subject	oplication. If not included n will be mailed in due course. THIS
1. 🔀 This communication is responsive to amendment filed Mar	<u>rch 7, 2008</u> .	
2. 🔀 The allowed claim(s) is/are <u>1-5, 7-16, 19, 20, 22-56 (renum</u>	nbered 1-52 <u>)</u> .	
 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 	e been received.	
3. Copies of the certified copies of the priority do	cuments have been received in this	national stage application from the
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be subm	IENT of this application.	
INFORMAL PATENT APPLICATION (PTO-152) which give		
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.	
(a) including changes required by the Notice of Draftspers	son's Patent Drawing Review(PTC	-948) attached
1) ☐ hereto or 2) ☐ to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t		
 DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT 		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. ☐ Notice of Informal	Patent Application
 Notice of Neterences Gled (110-092) Dotice of Draftperson's Patent Drawing Review (PTO-948) 	6. ☐ Interview Summary	/ (PTO-413),
3. ☐ Information Disclosure Statements (PTO/SB/08),	Paper No./Mail Da 7. ⊠ Examiner's Amend	ate ment/Comment
Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛛 Examiner's Statem	ent of Reasons for Allowance
or biological Material	9.	

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DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone communication with J. Mark Han (Reg. No. 57,898) on April 14, 2008.

The application has been amended as follows:

In claim 4, line 14:

delete "frist" and

insert –first-.

In claim 8, line 22:

delete "frist" and

insert -first-.

In claim 9, line 24:

delete "frist" and

insert –first-.

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In claim 11, line 10:

delete "frist" and

insert –first-.

In claim 19, line 9:

delete "frist" and

insert –first-.

In claim 23 line 18:

delete "frist" and

insert –first-.

Allowable Subject Matter

- 2. Claims 1-5, 7-16, 19, 20, 22-56 (renumbered 1-52) are allowed.
- 3. The following is an examiner's statement of reasons for allowance:

Regarding claims 1-3, claim 1 discloses a position-burst demodulator comprising an input circuit operable to receive even and odd samples of a first servo position burst, to add the even samples to generate a first sum and to add the odd samples to generate a second sum, an intermediate circuit coupled to the input circuit and operable to square the first and second sums, and to add the squared first and second sums to generate a third sum, an output circuit coupled to the intermediate circuit and operable to calculate the square root of the third sum to generate an output signal representing a magnitude of the first servo position burst, which is neither disclosed or an obvious variation of the prior art.

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Regarding claim 4, claim 4 discloses a position-burst demodulator, comprising an input circuit operable to receive and square first and second samples of a first servo position burst, an intermediate circuit coupled to the input circuit and operable to add the squared first and second samples to generate a first sum, an output circuit coupled to the intermediate circuit and operable to calculate the square root of the first sum, wherein the input circuit is operable to receive and square first and second samples of a second servo position burst, wherein the intermediate circuit is operable to add the squared first and second samples of the second servo position burst to generate a second sum, wherein the output circuit is operable to calculate the square root of the second sum, and a difference circuit operable to calculate a difference between the square roots of the first and second sums to generate an output signal representing an error between the first and second servo position bursts, which is neither disclosed or an obvious variation of the prior art.

Regarding claims 5, 7, and 32, claim 5 discloses a position-burst demodulator comprising a first adder operable to receive even and odd samples of a first servo position burst, to add the even samples together to generate a first sum, and to add the odd samples together to generate a second sum, a power circuit coupled to the first adder and operable to square the first sum and the second sum to respectively generate first and second squared sums, a second adder coupled to the power circuit and operable to add the first and second squared sums to generate a first sum of squares, and a root circuit coupled to the second adder and operable to calculate the square root of the first sum of squares to generate an output signal representing a magnitude of the first servo position burst, which is neither disclosed or an obvious variation of the prior art.

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Regarding claims 8, 9, and 33-36, claims 8 and 9 disclose a position-burst demodulator comprising a first adder operable to receive first and second sets of samples of a first servo position burst, to add the samples in the first set together to generate a first sum, and to add the samples in the second set together to generate a second sum, a power circuit coupled to the first adder and operable to square the first sum and the second sum to respectively generate first and second squared sums, a second adder coupled to the squarer and operable to add the first and second squared sums to generate a first sum of squares; a root circuit coupled to the second adder and operable to calculate the square root of the first sum of squares; wherein the first adder is operable to receive first and second sets of samples of a second servo position burst, to add the samples in the first set together to generate a third sum, and to add the samples in the second set together to generate a fourth sum; wherein the power circuit is operable to square the third sum and the fourth sum to respectively generate third and fourth squared sums; wherein the second adder is operable to add the third and fourth squared sums to generate a second sum of squares, wherein the root circuit is operable to calculate the square root of the second sum of squares, and a difference circuit coupled to the root circuit and operable to calculate a difference between the square roots of the first and second sums of squares to generate an output signal representing an error between the first and second servo position bursts, which is neither disclosed or an obvious variation of the prior art.

Regarding claims 10, 16, 37, 38, 43, and 44 claims 10 and 16 disclose a circuit and corresponding method for adding even samples of a first servo position burst to generate a first sum, adding odd samples of the first servo position burst to generate a second sum, squaring the first and second sums, adding the squared first and second sums to generate a third sum, and

calculating the square root of the third sum to generate an output signal representing a magnitude of the first servo position burst, which is neither disclosed or an obvious variation of the prior art.

Regarding claims 11 and 19, claims 11 and 19 disclose a circuit and corresponding method for squaring first and second samples of a first servo position burst, adding the squared first and second samples to generate a first sum, calculating the square root of the first sum, squaring first and second samples of a second servo position burst, adding the squared first and second samples of the second servo position burst to generate a second sum, calculating the square root of the second sum, and calculating a difference between the square roots of the first and second sums to generate an output signal representing an error between the first and second servo position bursts, which is neither disclosed or an obvious variation of the prior art.

Regarding claims 20, 22, 45, and 46, claim 20 discloses a method comprising receiving even and odd samples of a first servo position burst, adding the even samples together to generate a first sum, adding the odd samples together to generate a second sum, squaring the first and second sums to respectively generate first and second squared sums, adding the first and second squared sums together to generate a first sum of squares, and calculating the square root of the first sum of squares to generate an output signal representing a magnitude of the first servo position burst, which is neither disclosed or an obvious variation of the prior art.

Regarding claims 23, 47, and 48, claim 23 discloses a method comprising receiving first and second sets of samples of a first servo position burst, adding the samples in the first set together to generate a first sum, adding the samples in the second set together to generate a second sum, squaring the first and second sums to respectively generate first and second squared sums, adding the first and second squared sums together to generate a first sum of squares,

calculating the square root of the first sum of squares, receiving first and second sets of samples of a second servo position burst, adding the samples in the first set together to generate a third sum, adding the samples in the second set together to generate a fourth sum, squaring the third sum and the fourth sum to respectively generate third and fourth squared sums, adding the third and fourth squared sums to generate a second sum of squares, calculating the square root of the second sum of squares, and calculating a difference between the square roots of the fist and second sums of squares to generate an output signal representing an error between the first and second servo position bursts, which is neither disclosed or an obvious variation of the prior art.

Regarding claims 12-15, 24-29, 39-42, and 49-56, reasons for allowance are as discussed in the previous Office action mailed October 18, 2007.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniell L. Negrón whose telephone number is (571) 272-7559. The examiner can normally be reached on Monday-Friday (8:30am-5:00pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Joseph H. Feild can be reached on (571) 272-4090. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated

information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Joseph H. Feild/ Supervisory Patent Examiner, Art Unit 2627

/D. L. N./ Examiner, Art Unit 2627 April 7, 2008